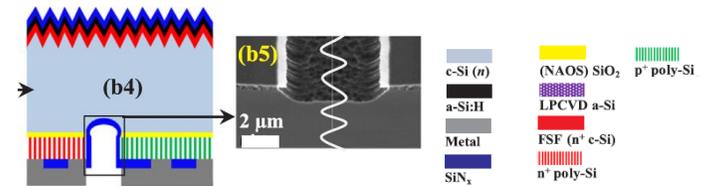
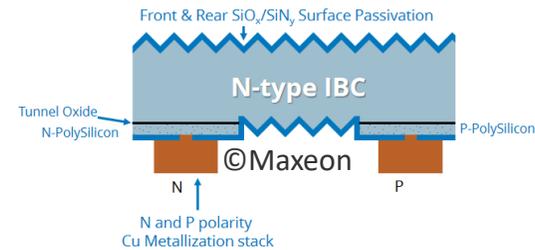
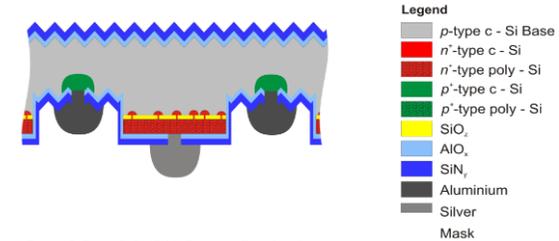
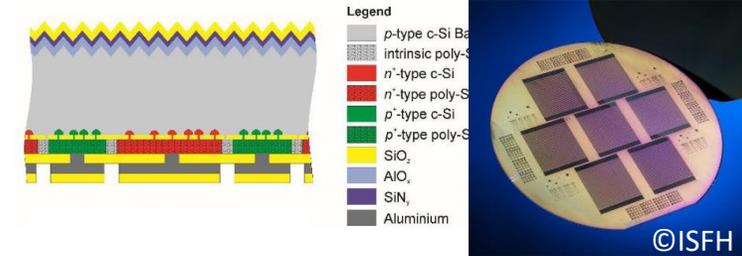


# polyZEBRA – A both polarity passivating contact IBC cell

J. Linke, F. Buchholz, C. Peter, J. Hoß, J. Lossen

# Overview poly-Si based IBC concepts

- ISFH: p-IBC record cell both polarities poly-Si<sup>[1]</sup>
  - Industrially irrelevant
- ISFH: POLO-IBC current development with n<sup>+</sup> poly-Si and p Al-BSF<sup>[2]</sup>
  - (see talk Verena Mertens this workshop)
- SunPower (Maxeon), LG: Both polarities poly-Si/SiO<sub>x</sub>
  - Industrially established, details unknown
- TU Delft: Cell area 2cm<sup>2</sup> in 2018<sup>[3]</sup>, no following publication found



[1] Haase, F. et al., *Sol. Energy Mater. Sol. Cells* 186 (2018): 184-193.

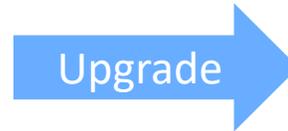
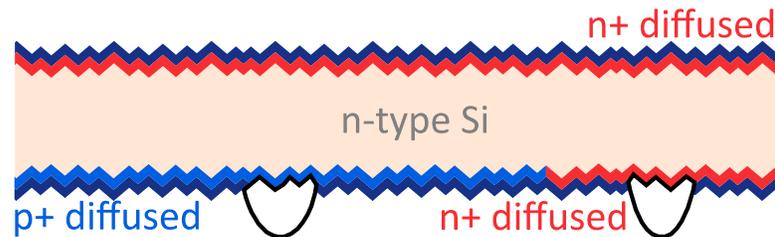
[2] Patented by Bende, E., Wu, Y., Anker, J., EP3685445A1.

[3] Yang, G. et al., *Sol. Energy Mater. Sol. Cells* 186 (2018): 9-13.

# From ZEBRA to polyZEBRA

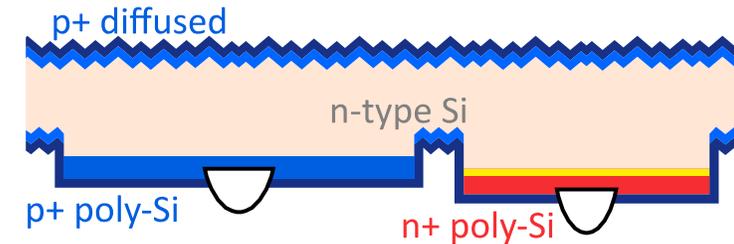
## ZEBRA

- p/n regions diffused
- Direct contact of p/n regions
- >24% in production (see talk Jan Lossen this workshop)

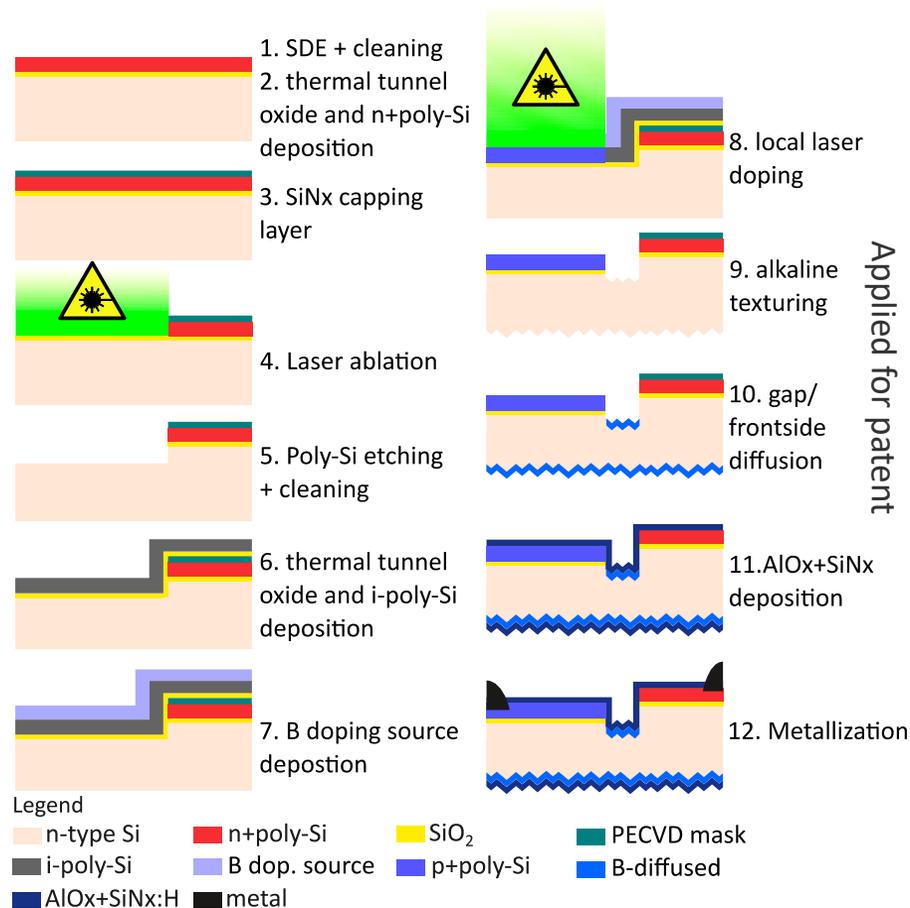


## polyZEBRA

- p/n regions poly-Si/SiO<sub>x</sub> contacts
- Textured gap between p/n regions
- p<sup>+</sup> diffused gap and frontside



# polyZEBRA process flow



## Key processes:

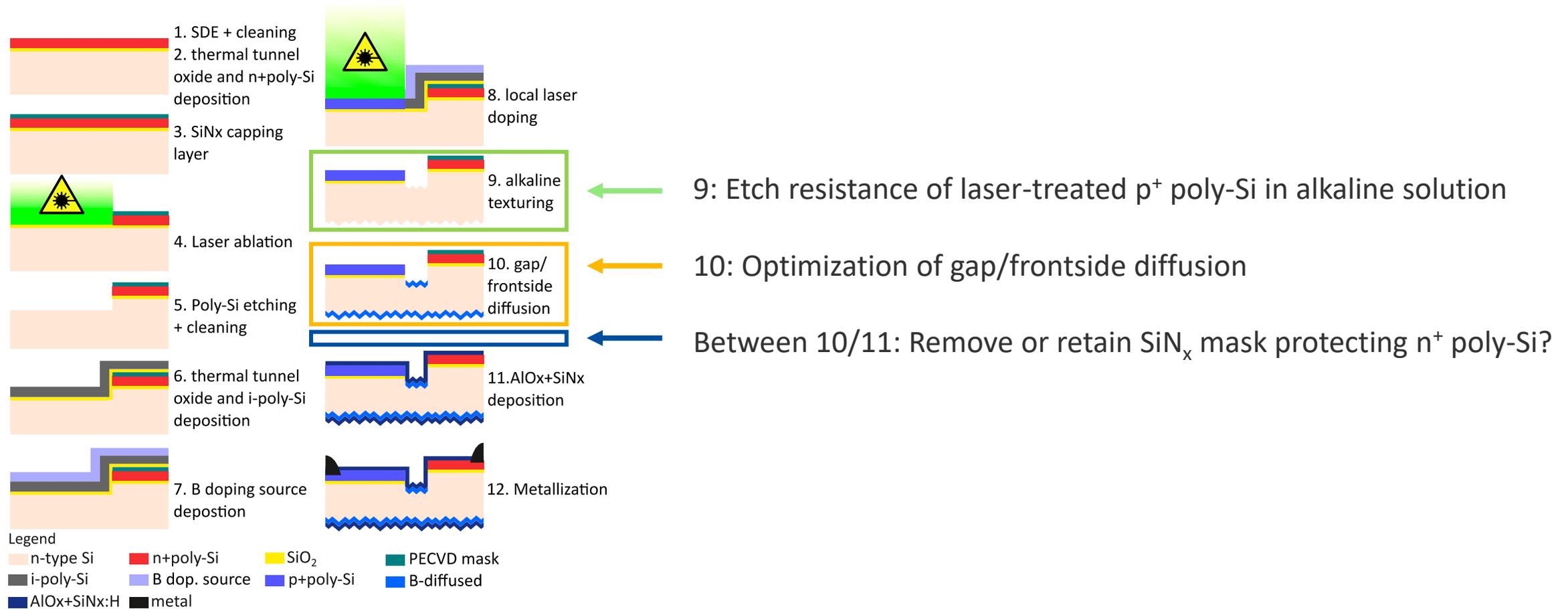
- n<sup>+</sup> poly-Si patterning by local laser mask ablation (4.)
- p<sup>+</sup> poly-Si patterning by local laser activation of inactive dopants (8.)  
→ High etch selectivity in alkaline solution<sup>[4]</sup> (9.)
- p-diffused gap ensures soft breakdown<sup>[5]</sup> (10.)
- Simultaneous printing of both polarities<sup>[6]</sup> (12.)

## Compatible with industrially established ZEBRA process:

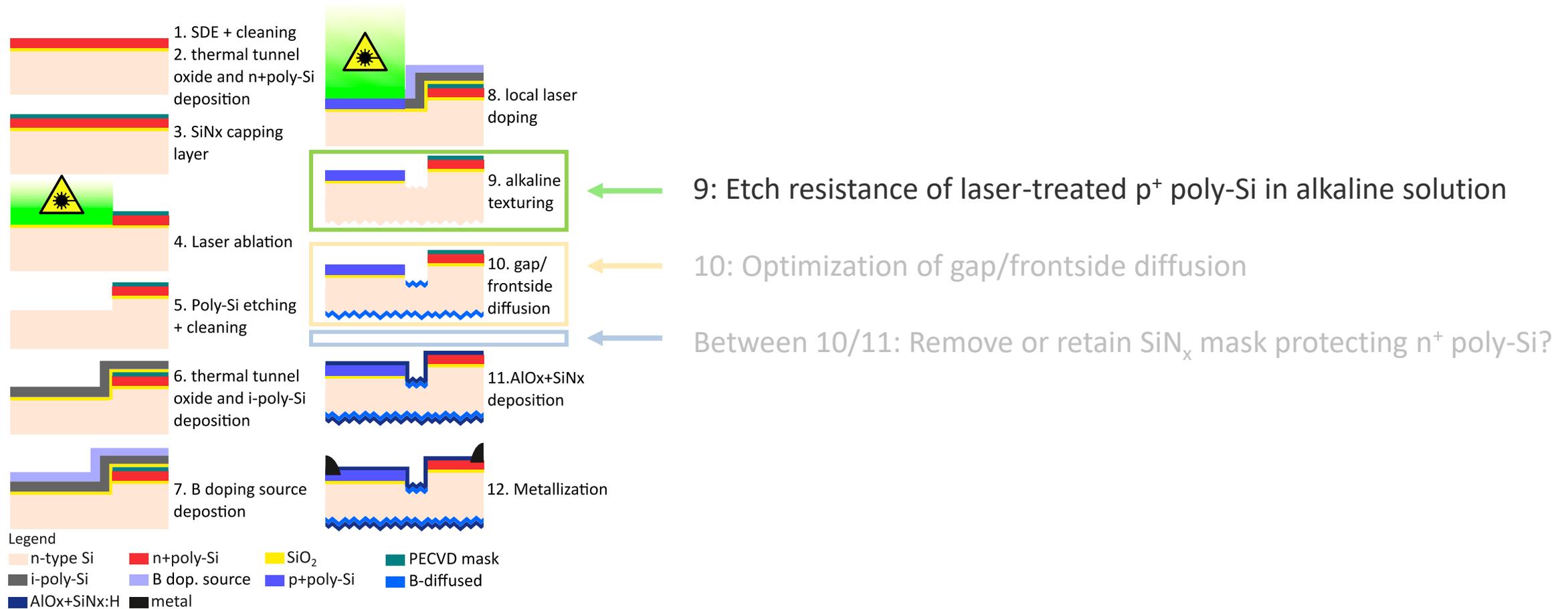
- Only few additional steps
- Standard ZEBRA metallization scheme
- 23.4% efficiency demonstrated with industrially relevant tools on full-area M6 wafer format  
→ Upgrade from ZEBRA production line to polyZEBRA possible

[4] Buchholz, F. et al., *Proc. 38th EUPVSEC* (2021): 140-143. [5] Linke, J. et al., *Silicon PV* (2022): to be published. [6] Buchholz, F., Linke, J. et al., *Proc. WCPEC-8* (2022): to be published.

# polyZEBRA process flow



# polyZEBRA process flow



# Etch resistance of laser-treated p<sup>+</sup> poly-Si in alkaline solution

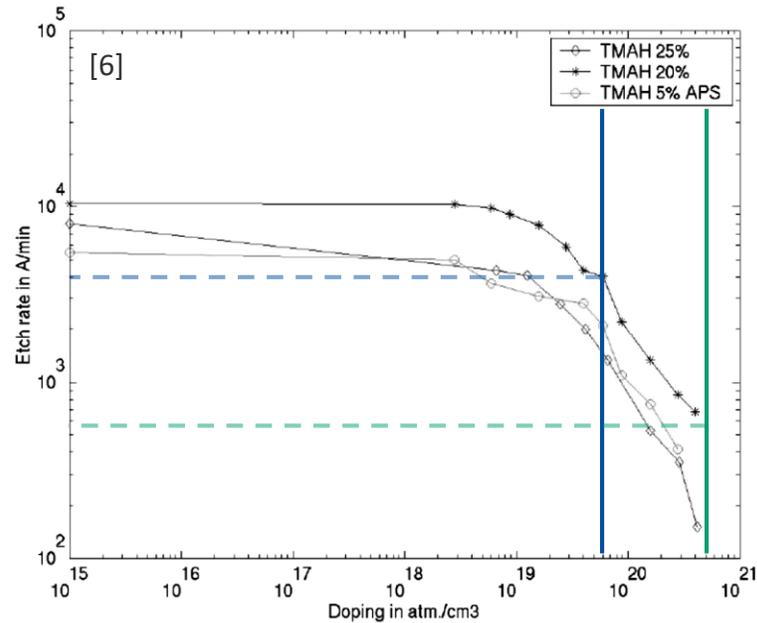
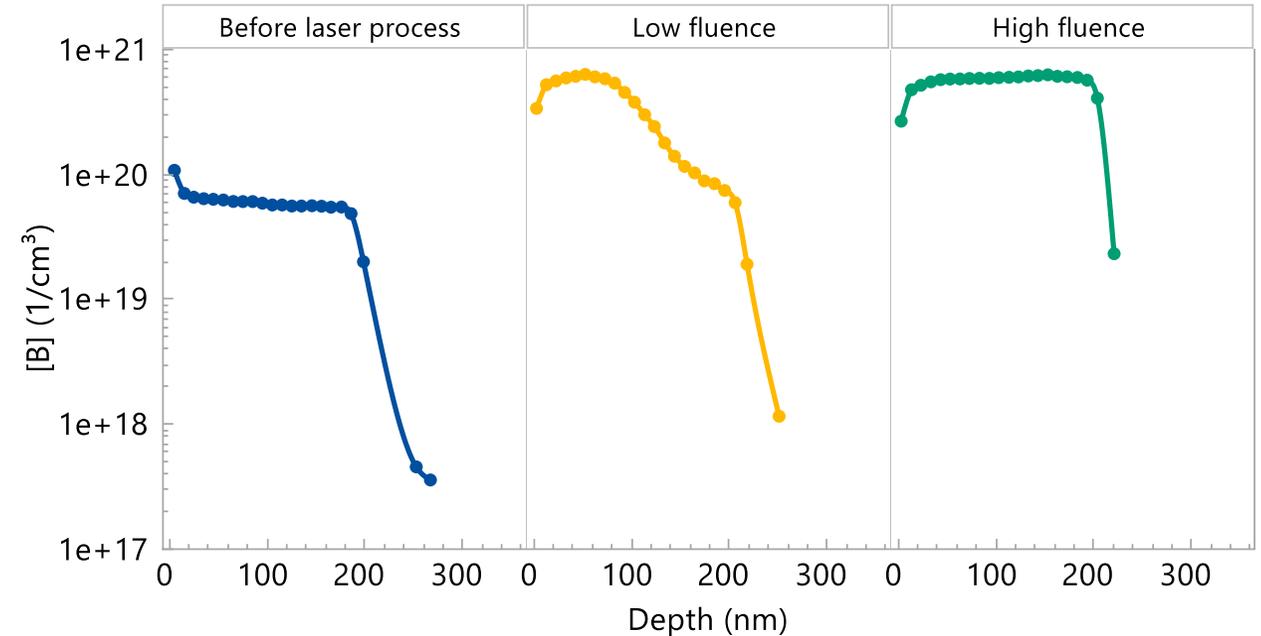


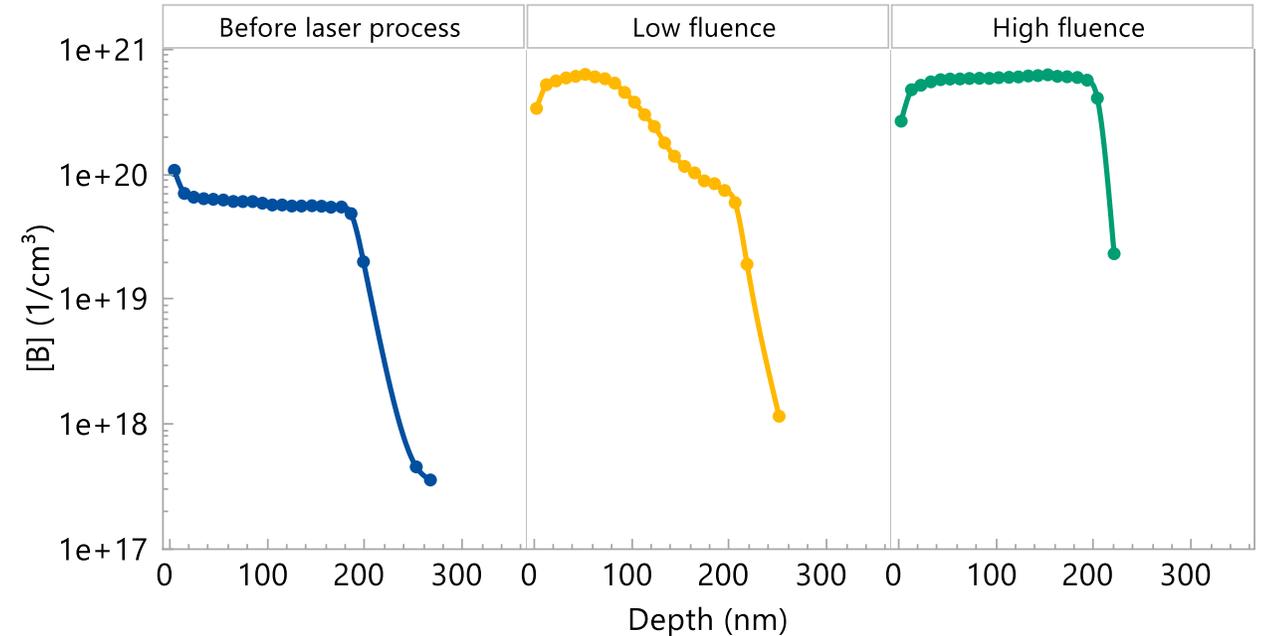
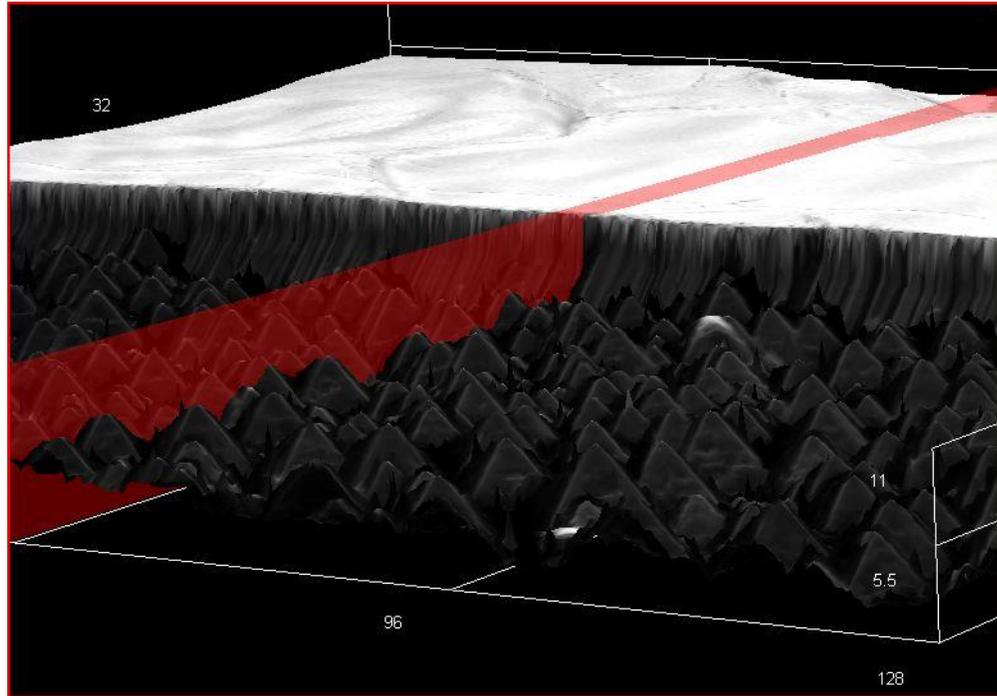
Figure 8: Polysilicon etch rate in different solutions.



- Homogeneous B activation possible
- Gain factor  $\sim 10$  in active [B]  $\rightarrow$  Factor  $\sim 7$  lower etch rate (according to [7])
- Measured etch selectivity 250-500

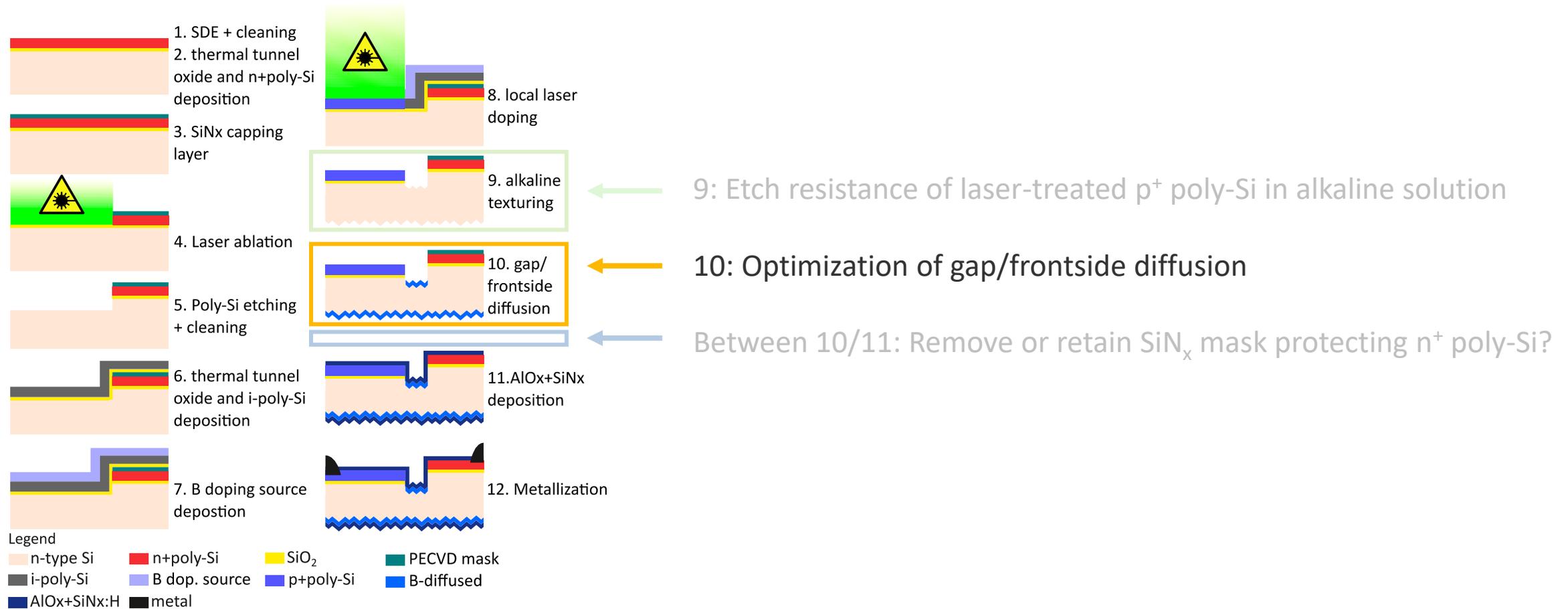
[7] Charavel, R. et al., *Proc. SPIE* 5116 (2003): 699-709.

# Etch resistance of laser-treated p<sup>+</sup> poly-Si in alkaline solution



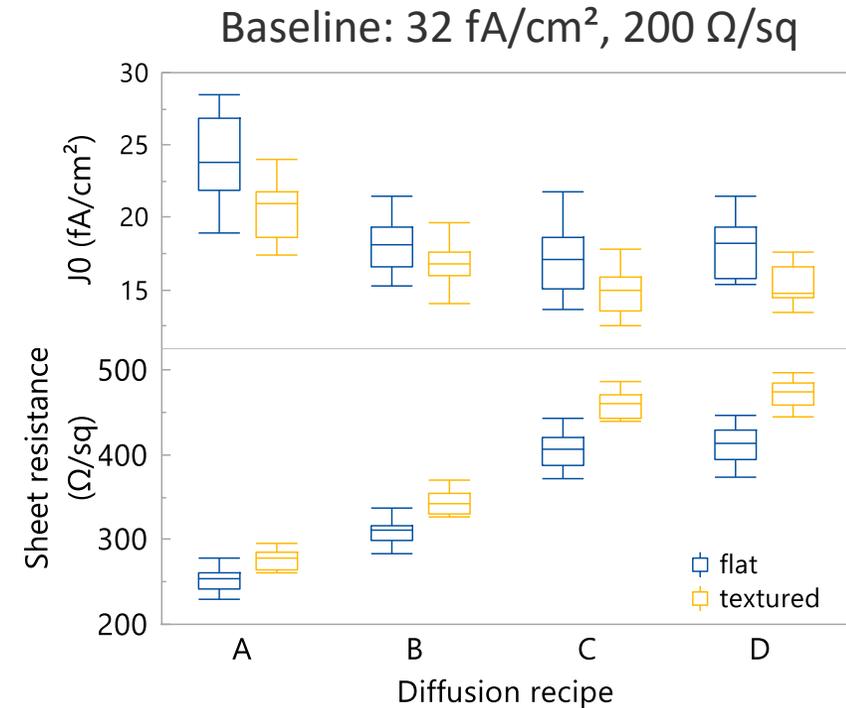
- Homogeneous B activation possible
- Gain factor ~10 in active [B] → Factor ~7 lower etch rate (according to [7])
- Measured etch selectivity 250-500

# polyZEBRA process flow



# Optimization of gap/frontside diffusion

Recipe	Depo	Drive-in
<b>Baseline</b>	<b>840-900°C, 16 min</b>	<b>985°C, 20min</b>
A	830°C, 16 min	960°C, 5 min
B	820°C, 16 min	950°C, 20 min
<b>C</b>	<b>820°C, 16 min</b>	<b>925°C, 20 min</b>
D	810°C, 16 min	925°C, 20 min

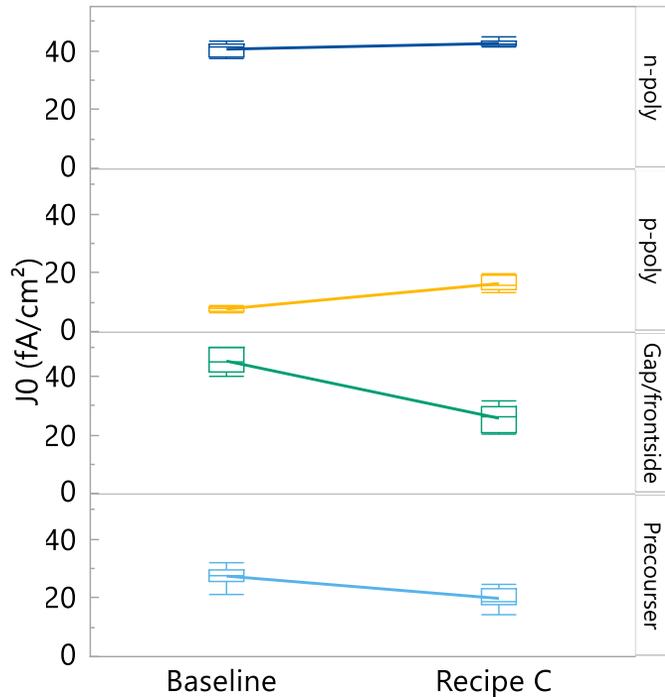


- Optimization towards  $J_0$ ,  $R_{\text{sheet}}$  plays minor role
- Recipe C chosen for cell fabrication:  
Lowest  $J_0$  on textured surface, slightly lower  $R_{\text{sheet}}$  than recipe D

[6] Buchholz, F., Linke, J. et al., *Proc. WCPEC-8 (2022)*: to be published

# Optimization of gap/frontside diffusion

Test structures:

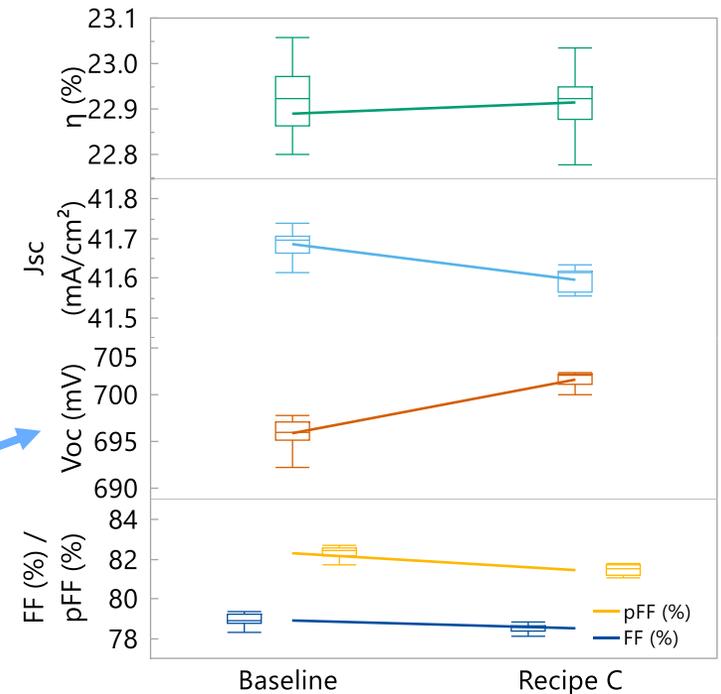


Lower  $J_0$  in gap/frontside for recipe C

Area fraction gap/frontside: ~55%  
 → Lower  $J_0$  on precursors

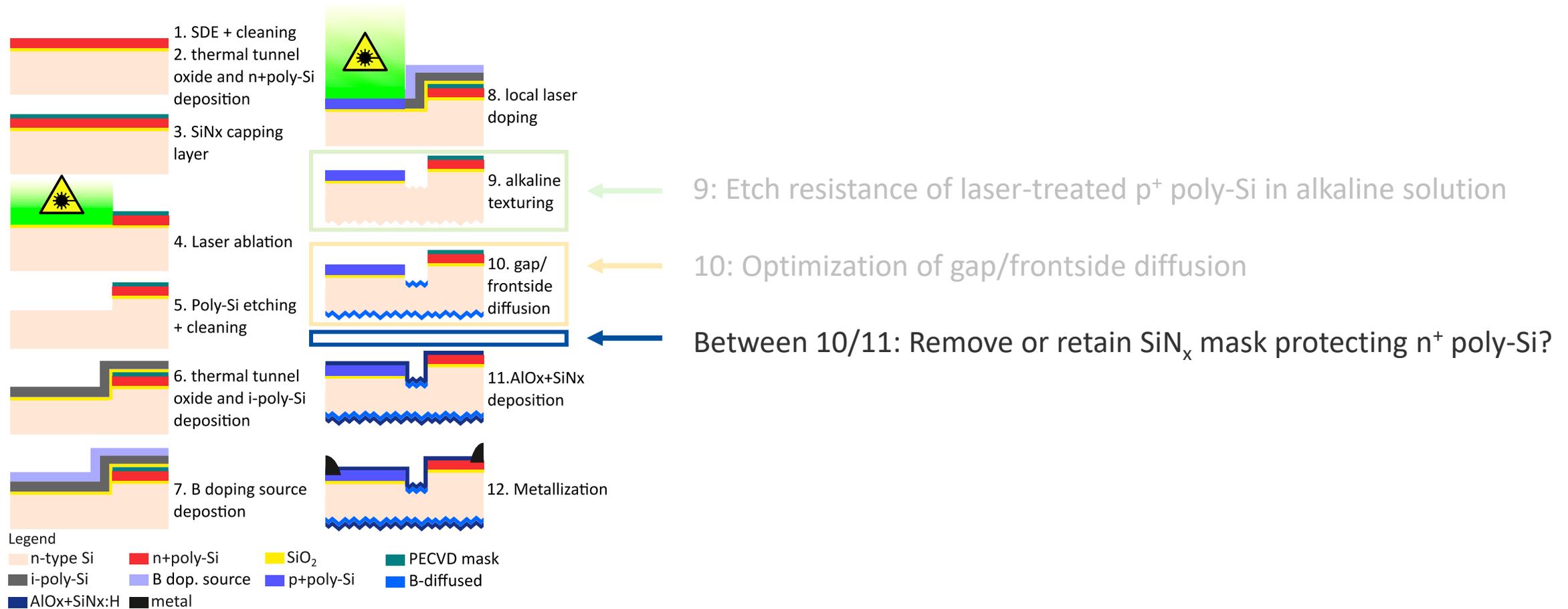
Consequently higher  $V_{oc}$   
 (but similar  $\eta$  due to lower pFF)

polyZEBRA cells:



[6] Buchholz, F., Linke, J. et al., *Proc. WCPEC-8*: to be published

# polyZEBRA process flow

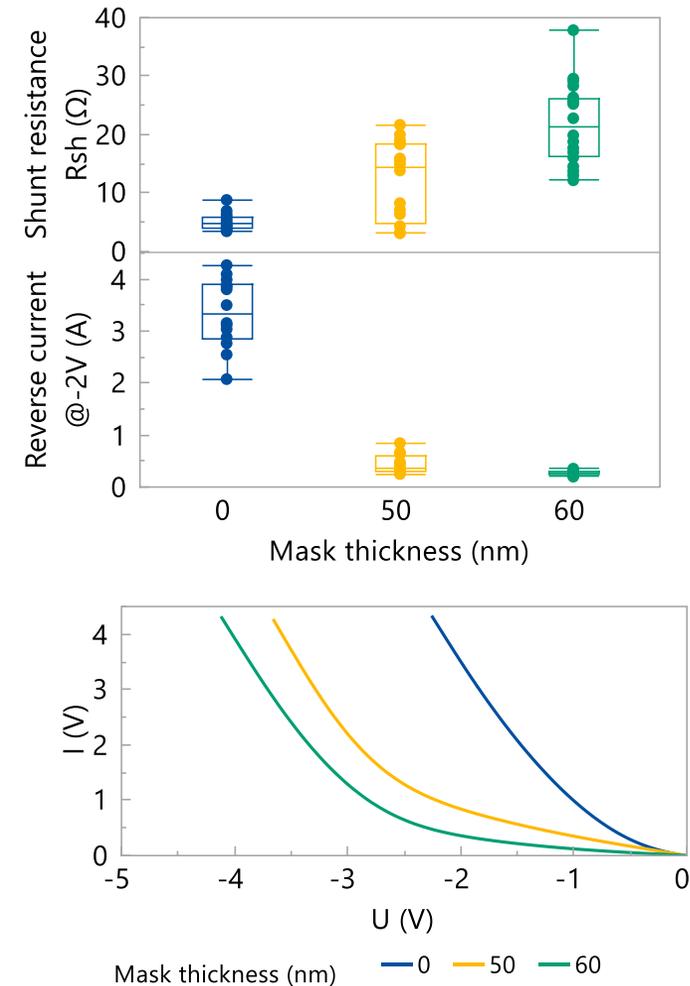


# Remove or retain SiN<sub>x</sub> mask protecting n<sup>+</sup> poly-Si?

Background: Mask removal would be one extra process step

## Reverse bias characteristics of polyZEBRA cells:

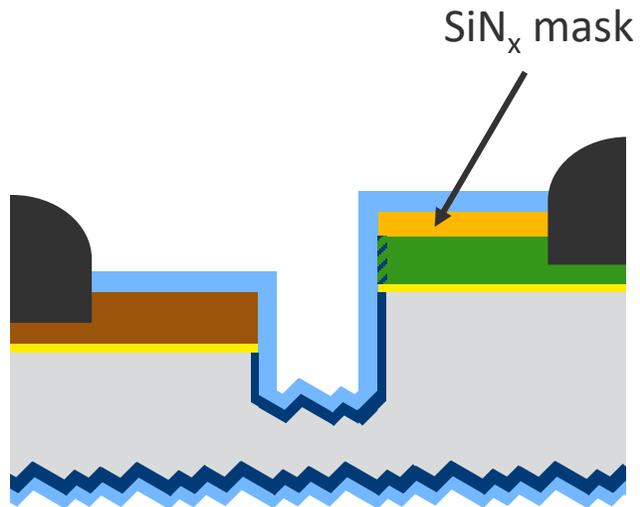
- Shunt resistance reduces with thinner mask
- Early breakdown with non-diode behaviour after mask removal (0 nm thickness)
  - Mask blocks shunting
  - Mask mandatory for soft breakdown



[5] Linke, J. et al., *Silicon PV* (2022): to be published

# Remove or retain SiN<sub>x</sub> mask protecting n<sup>+</sup> poly-Si?

Mask retaining

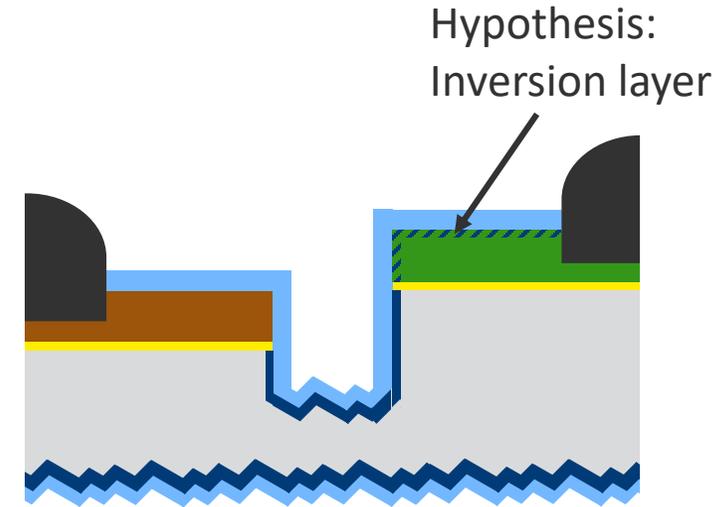


■ n c-Si  
■ SiO<sub>x</sub>

■ n<sup>+</sup> poly-Si  
■ p<sup>+</sup> poly-Si

■ p-diffused  
■ SiN<sub>x</sub> mask

Mask removal

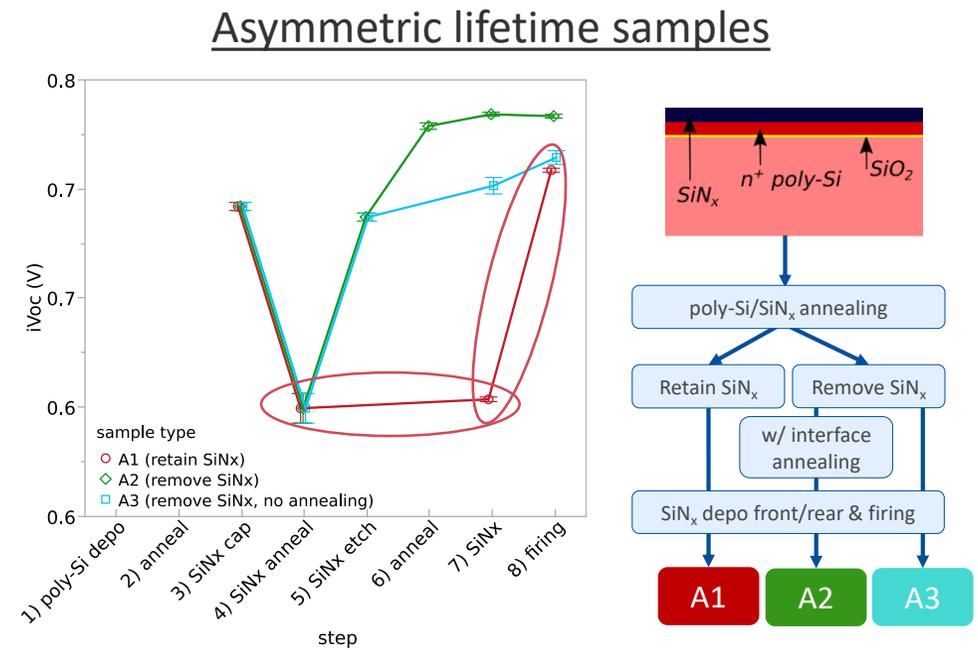
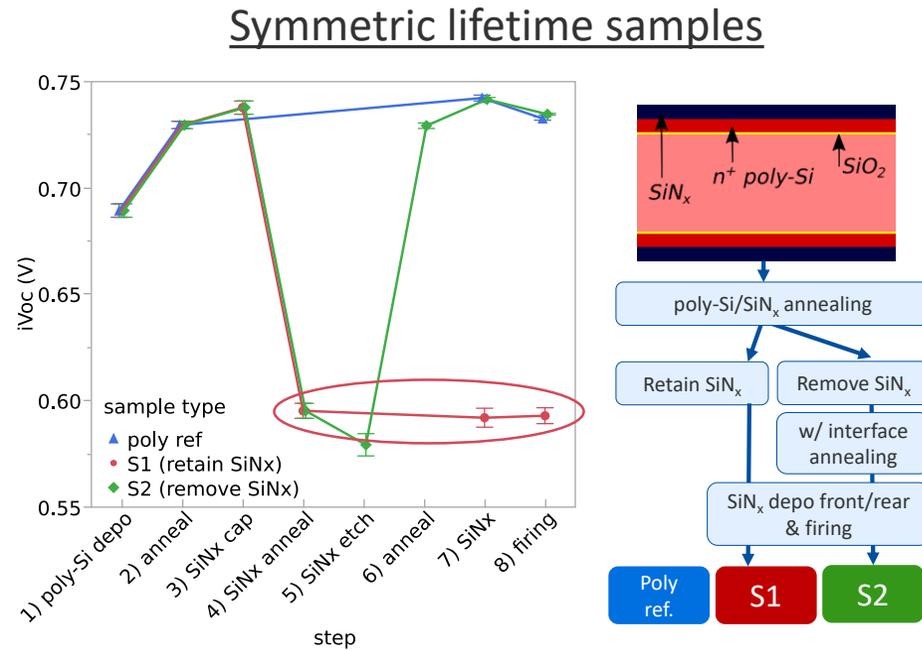


■ AlO<sub>x</sub>/SiN<sub>y</sub>  
■ Metal

Conclusion: Mask retaining is beneficial

[5] Linke, J. et al., *Silicon PV* (2022): to be published

# Remove or retain SiN<sub>x</sub> mask protecting n<sup>+</sup> poly-Si?

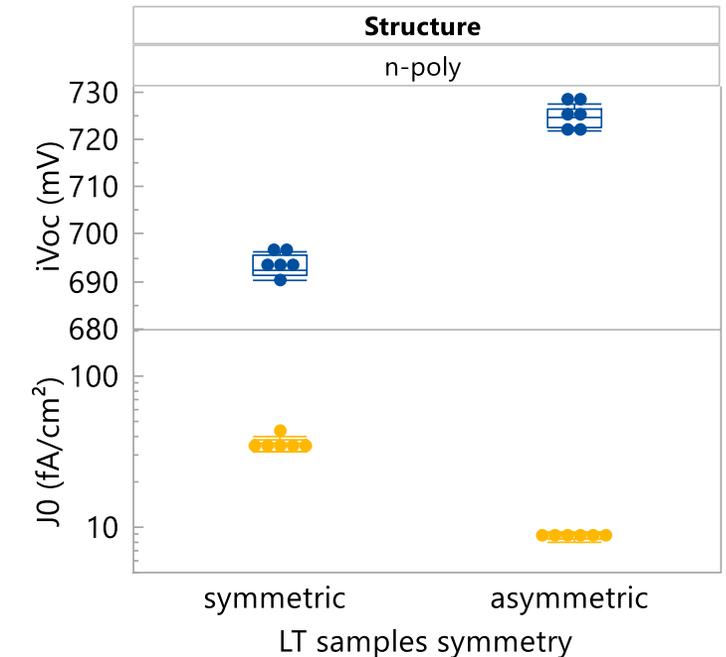
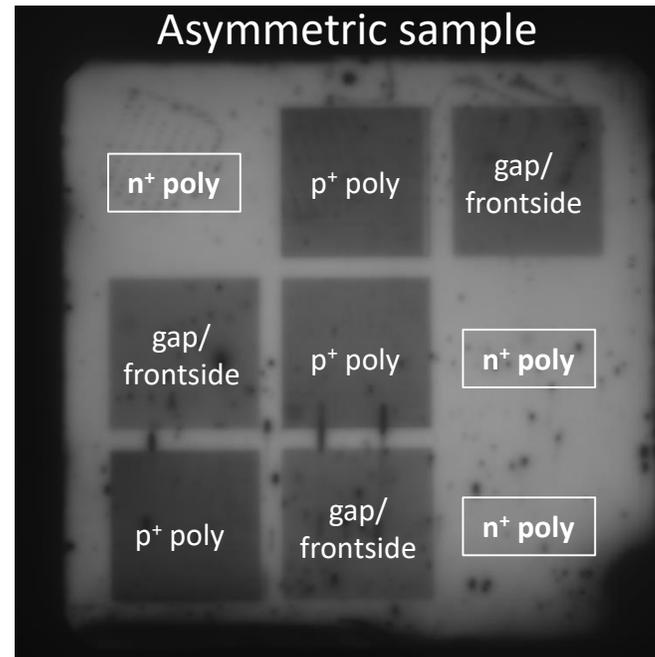
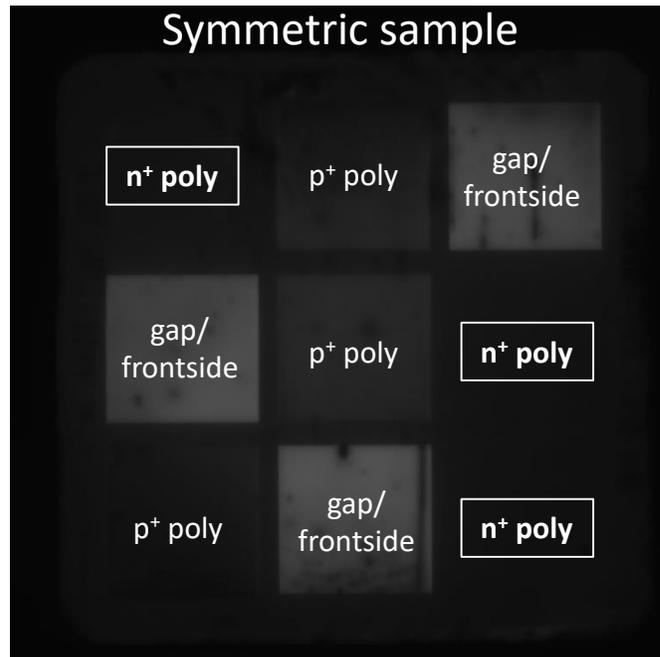


- SiN<sub>x</sub>/n<sup>+</sup> poly-Si stacks degrade during tube furnace anneal to ~600mV (all groups)
- Recovery possible by SiN<sub>x</sub> etch + 2<sup>nd</sup> tube anneal (S2,A2) or by firing of asymmetric lifetime samples (A1)
- polyZEBRA cell structure matches asymmetric lifetime samples A1 → **Mask retaining possible?**

[8] Hoss, J. et al., *Proc. WCPEC-8*: to be published

# Remove or retain SiN<sub>x</sub> mask protecting n<sup>+</sup> poly-Si?

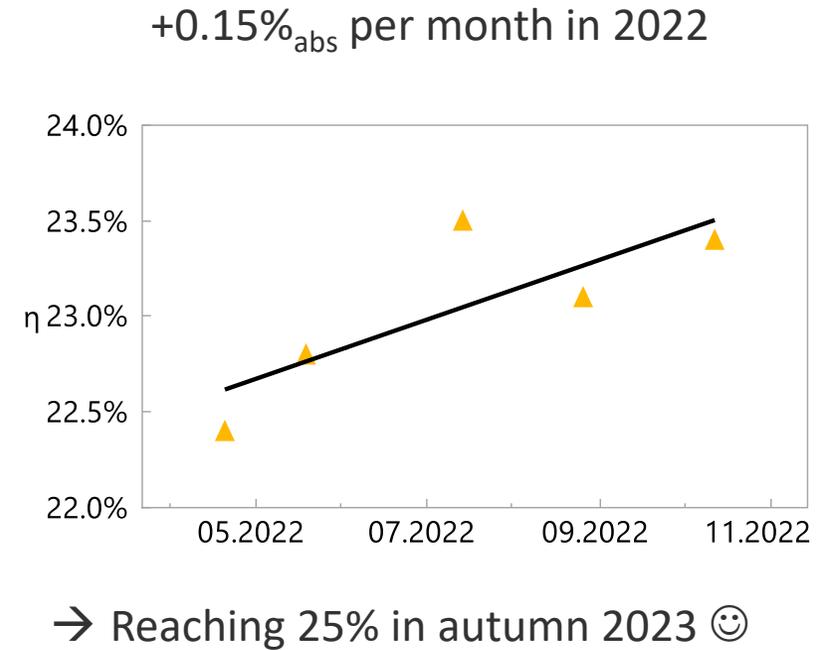
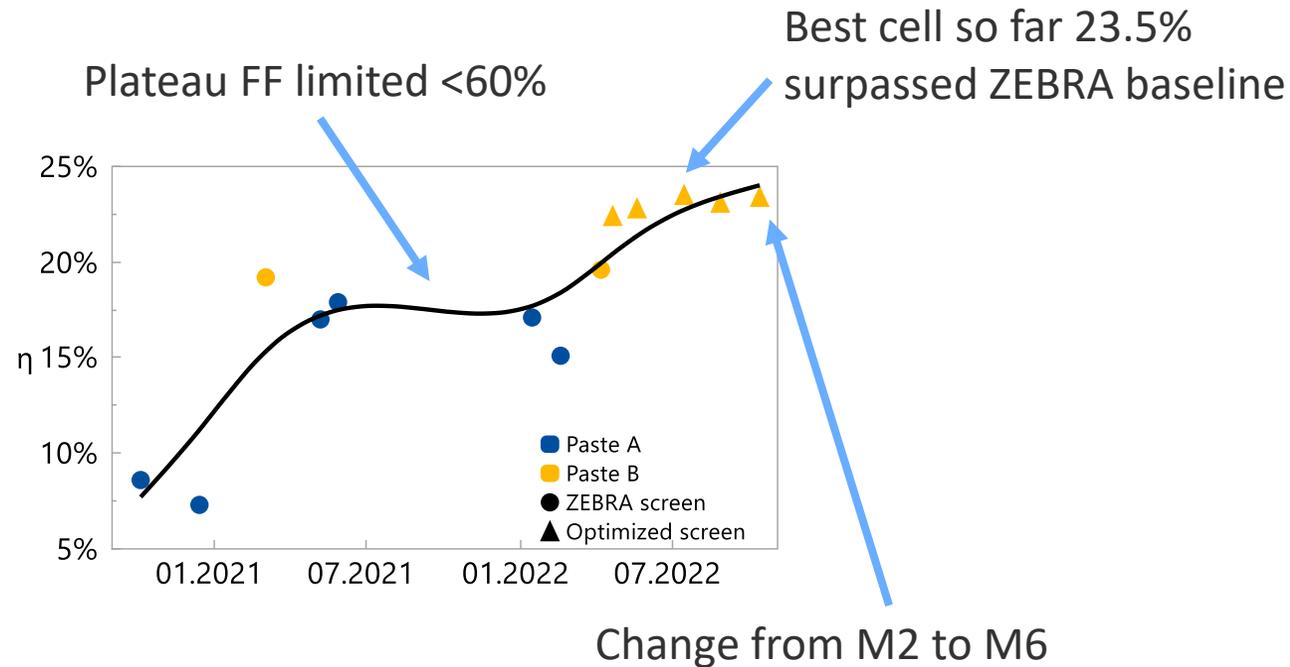
- Lifetime samples according to polyZEBRA cell process with SiN<sub>x</sub> mask retained:



- Degradation of symmetric samples and recovery of asymmetric samples confirmed for cell process

Asymmetric samples match cell structure → polyZEBRA cells not affected → Mask will be retained in future

# Learning curve

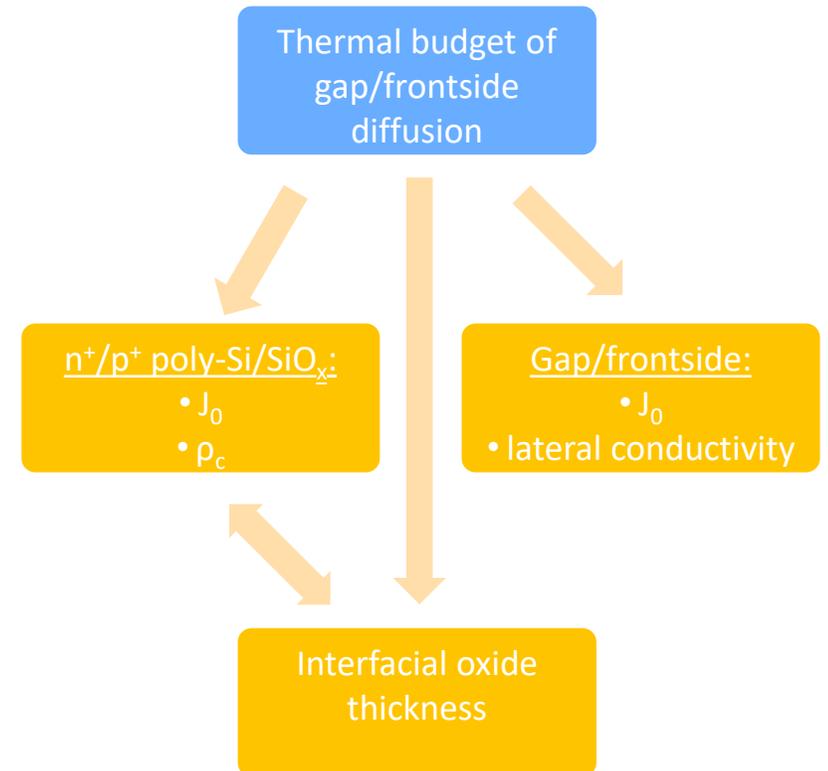


Current champion cell on full-area M6 wafer format:

$\eta$ (%)	$V_{oc}$ (mV)	$J_{sc}$ (mA/cm <sup>2</sup> )	FF (%)	pFF (%)	$iV_{oc}$ (mV)	$iV_{oc}-V_{oc}$ (mV)	pFF-FF (mV)
23.4	703	41.9	79.3	82.3	712	9	3

# Challenges and next steps

- $V_{oc}$  / pFF limited
- Co-optimization of p-diffusion for gap/frontside and  $p^+/n^+$  poly-Si/SiO<sub>x</sub>:
  - Gap/frontside  $J_0$
  - Gap/frontside lateral conductivity (soft breakdown)
  - Thermal budget for  $n^+/p^+$  poly-Si passivation ( $J_0$ )
  - Interfacial oxide thickness ( $J_0/\rho_c$ )
- Replace LPCVD poly-Si deposition by PECVD
- Replace BBr<sub>3</sub> ex-situ doping of  $p^+$  poly-Si by in-situ doped PECVD  $p^+$  a-Si or BSG / i a-Si stacks
- Fine-tuning (optical management,  $n^+/p^+$  poly-Si and gap width, metallization, Ag reduction, ...)





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Thank you for your  
attention

**LONGI**

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